

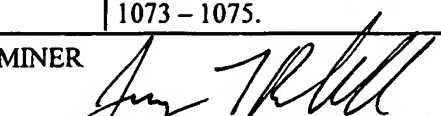


SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION		ATTY. DOCKET NO. SUN-P9705		APPLICATION NO. 10/816,762			
PTO-1449		APPLICANT Robert J. Drost, et al.		FILING DATE April 2, 2004			
		GROUP ART UNIT 2874					
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
JTR	6,916,719	Jul. 12, 2005	Knight et al.	438	381	12/10/99	
JTR	5,629,838	May 13, 1997	Knight et al.	361	782	6/24/94	
JTR	6,728,113	Apr. 27, 2004	Knight et al.	361	760	6/24/93	
<div style="text-align: center;">X</div>							
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<div style="text-align: center;">X</div>							
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
<div style="text-align: center;">X</div>							
EXAMINER			DATE CONSIDERED				
			12/2/05				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b>  PTO-1449				ATTY. DOCKET NO. SUN-P9705		APPLICATION NO. To Be Assigned	
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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
JTR	Publication entitled "Optical Receivers for Optoelectronic VLSI" by Ted K. Woodward et al., IEEE Journal of Selected Topics in Quantum Electronics, Vol. 2, No. 1, April 1996, pgs. 106 – 116.						
JTR	Publication entitled "16 x 16 VCSEL Array Flip-Chip Bonded to CMOS VLSI Circuit", by A.V. Krishnamoorthy et al., IEEE Photonics Technology Letters, Vol. 12, No. 8, August 2000, pgs. 1073 – 1075.						
EXAMINER 				DATE CONSIDERED 12/2/05			

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
JTR	5,237,434	August 17, 1993	Feldman et al.	359	19	11/5/1991	
JTR	5,857,042	January 5, 1999	Robertson et al.	385	33	4/29/1997	
JTR	4,281,253	July 28, 1981	Culver	250	551	8/29/1978	
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<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<del> </del>							
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<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
JTR	Publication entitled "Interconnect density capabilities of computer generated holograms for optical interconnection of very large scale integrated circuits" by Feldman et al., XP 000033764, Applied Optics, Vol. 28, No. 15, 1 August 1989, pgs. 3134 to 3137.						
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EXAMINER <i>Jy TRH</i>				DATE CONSIDERED 12/2/05			

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